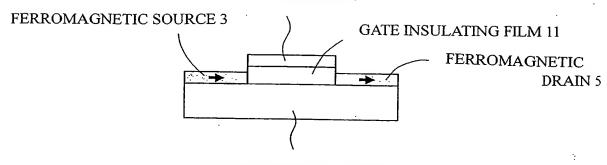
FIG. 1

GATE ELECTRODE 7

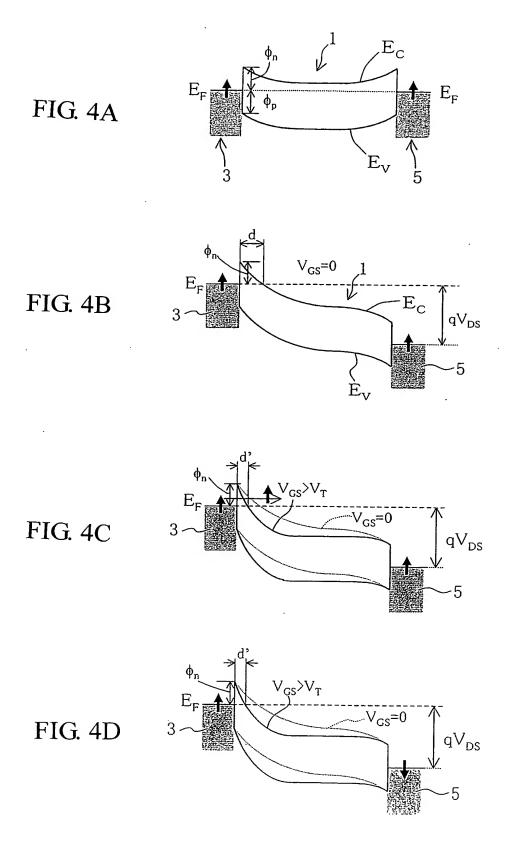


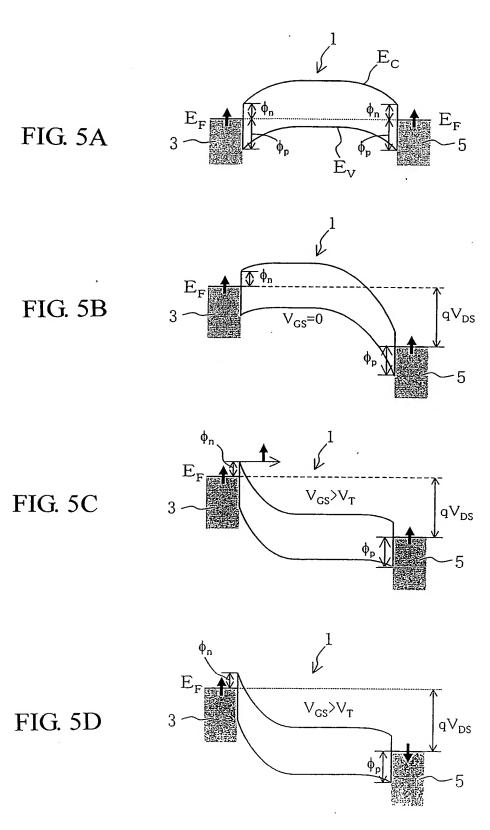
SEMICONDUCTOR LAYER 1

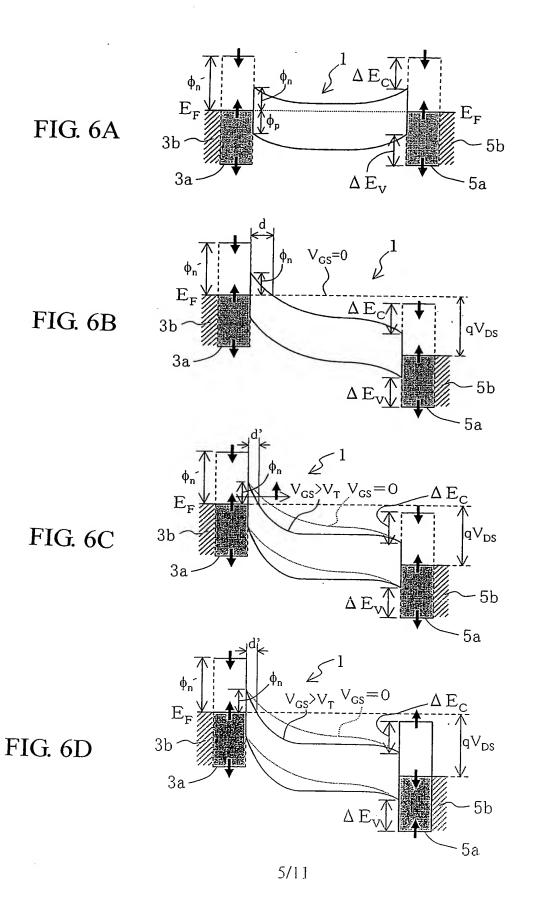
FIG. 2A E_{G} Ev Ec E_G FIG. 2B E_{F} E_CHM E_{F} 3b \sim FIG. 3A E_{G} · 5b 3a E_VHM 5a E_{C}^{HM} $\Delta_{L}E_{C}$ E_{G} E_{F} FIG. 3B

5a

За







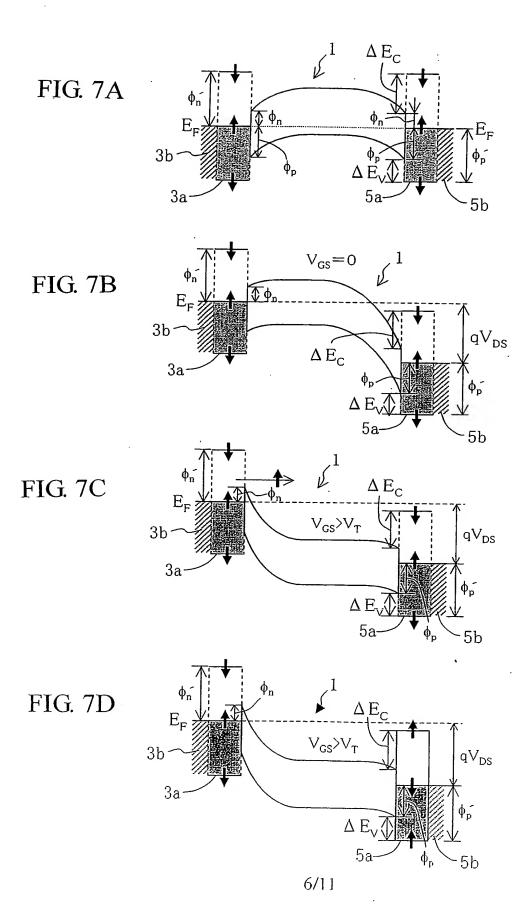
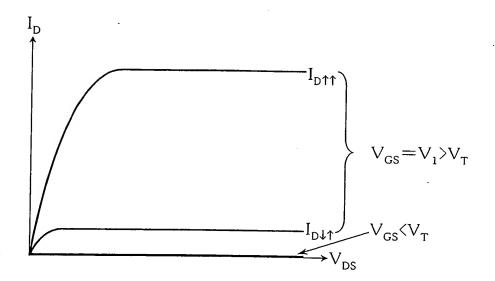
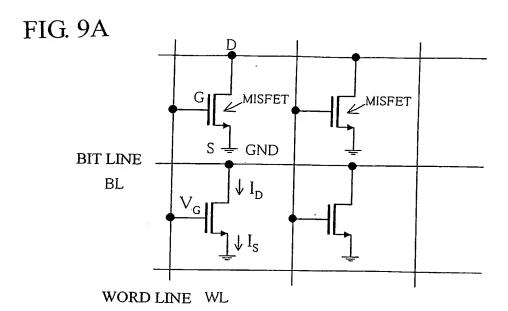
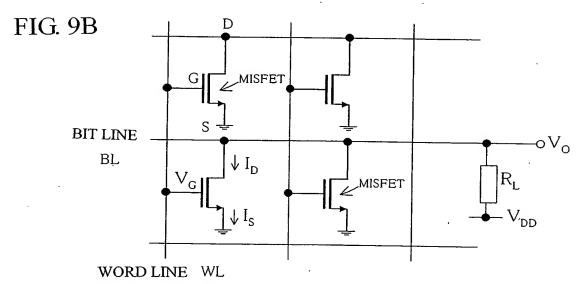
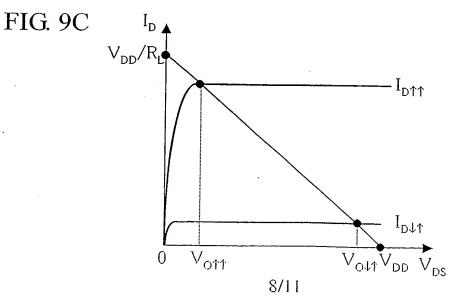


FIG. 8











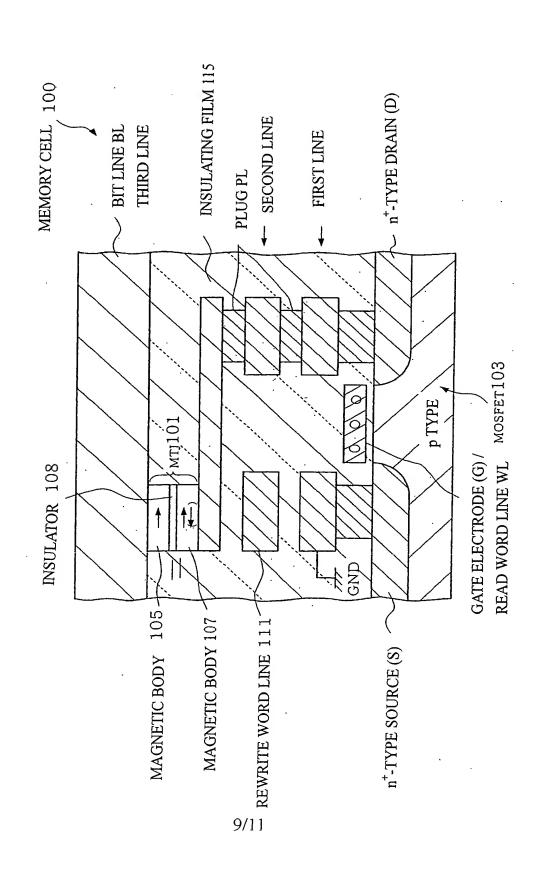
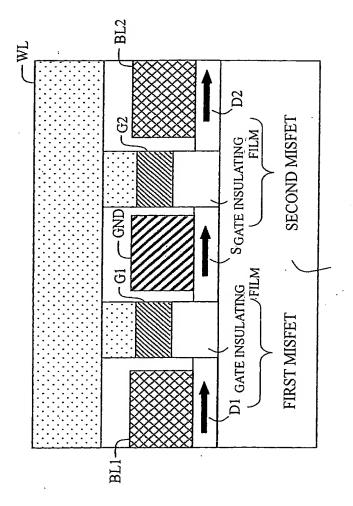


FIG. 11



SEMICONDUCTOR LAYER

FIG. 12

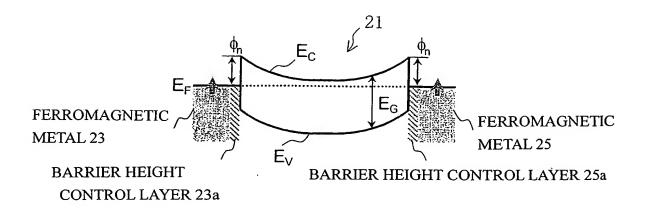


FIG. 13A

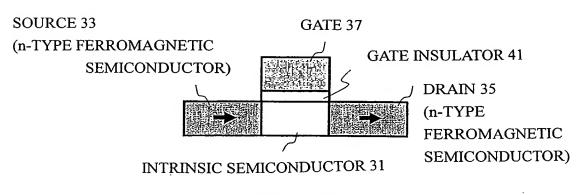
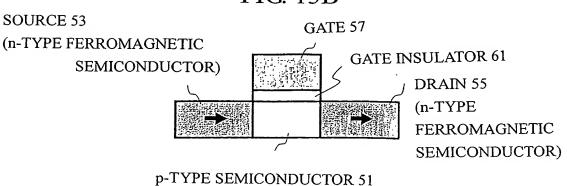


FIG. 13B



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